

## DP-37033-4 / DP-37033-8

## QUICKSTART GUIDE

Document Control No. DP-37033-4/-8 REV A

22 June 2023



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### DOCUMENT REVISION HISTORY

Rev	DCN	Date	Description
A	-	22 June 23	First Draft for Quickstart Guide

**Step 1:** Prior to connecting via your PC, the module should be connected like so. Voltage from your power supply is recommended to be set at <u>14.5VDC</u>.



370	33-4	370	33-8				
J1							
Connector	Function	Connector	Function				
J1-1	Node Power	J1-1	CAN High				
J1-2	CAN High	J1-2	NC				
J1-3	NC	J1-3	Node Power				
J1-4	CAN Low	J1-4	NC				
		J1-5	Shield				
		J1-6	CAN Low				

**Step 2:** Open DPNPlayer and click File > Open and select the **"35012-2H-CNFG.dpn"** file.

Open				Waiting for connection	۱ —			
Save Close All	1							
Recent Files								
Exit	le	Туре	Info	Output Name	Value	Туре	Info	
	_							

Step 3: Once the file is opened, this window will pop up. Click 'Check All' and then 'Synchronize'.

Name       ∧         ✓ CTRL1_0x52       ⊂         ✓ COMMAND_82       ✓         ✓ Save Configuration       ✓         ✓ ID1       ✓         ✓ COMMAND_83       ✓         ✓ COMMAND_83       ✓         ✓ H1-Current-Limit       ✓         ✓ COMMAND_83       ✓         ✓ H1-Current-Limit       ✓         ✓ COMMAND_84       ✓         ✓ H4-Current-Limit       ✓         ✓ H6-Current-Limit       ✓         ✓ H6-Current-Limit       ✓         ✓ H7-Current-Limit       ✓         ✓ H7-Current-Limit       ✓         ✓ H7-Current-Limit       ✓         ✓ H7-Current-Limit       ✓	nfo		Output Name V	alue Type	Info
Name <ul> <li>CTRL1_0x52</li> <li>COMMAND_82</li> <li>Save Configuration</li> <li>Unprocessed Analog Inputs</li> <li>ID1</li> <li>CTRL1_0x53</li> <li>COMMAND_83</li> <li>H1-Current-Limit</li> <li>H2-Current-Limit</li> <li>H2-Current-Limit</li> <li>GOMMAND_84</li> <li>COMMAND_84</li> <li>COMMAND_84</li> <li>GOMMAND_84</li> <li>GOMMAND_84</li> <li>GOMMAND_84</li> <li>H4-Current-Limit</li> <li>H5-Current-Limit</li> <li>H6-Current-Limit</li> <li>H7-Current-Limit</li> <li>H7-Current-Limit</li> <li>Check All</li> </ul>		🖳 DPN File	-	$\Box$ $\times$	
Check All Synchronize		Name         ✓ CTRL1_0x52         ✓ COMMAND_82         ✓ Save Configuration         ✓ Unprocessed Analog Inputs         ✓ ID1         ✓ CTRL1_0x53         ✓ COMMAND_83         ✓ H1-Current-Limit         ✓ H2-Current-Limit         ✓ CTRL1_0x54         ✓ COMMAND_84         ✓ H4-Current-Limit         ✓ H5-Current-Limit         ✓ H5-Current-Limit         ✓ H5-Current-Limit         ✓ H5-Current-Limit         ✓ H5-Current-Limit         ✓ H7-Current-Limit         ✓ H7-Current-Limit			
Synchronize				Check All	
			Synchronize		

**Step 3:** Once the correct DPN file has been opened, this is how the window should look.

Waiting for connection				Waiting for connection				
Input Name	Value	Туре	Info	Output Name	Value	Туре	Info	
AD2 - CNFG1	0	10-Bit-2		CTRL1 0x52 - COMMAND 82	82	8-Bit		
AD2 - VBAT	0	10-Bit-2		CTRL1 0x52 - Save Configuration	0	2-Bit		
AD2 - TEMP	0	10-Bit-2		CTRL1_0x52 - Unprocessed Analog Inputs	0	2-Bit		
AD3 - FET1x	0	16-Bit		CTRL1_0x52 - ID1	0	8-Bit		
AD3 - FET2x	0	16-Bit		CTRL1_0x53 - COMMAND_83	83	8-Bit		
AD3 - FET3x	0	16-Bit		CTRL1_0x53 - H1-Current-Limit	6000	16-Bit		
AD3 - FET4x	0	16-Bit		CTRL1_0x53 - H2-Current-Limit	6000	16-Bit		
AD4 - FET5x	0	16-Bit		CTRL1_0x53 - H3-Current-Limit	6000	16-Bit		
AD4 - FET6x	0	16-Bit		CTRL1_0x54 - COMMAND_84	84	8-Bit		
AD4 - FET7x	0	16-Bit		CTRL1_0x54 - H4-Current-Limit	6000	16-Bit		
AD4 - FET8x	0	16-Bit		CTRL1_0x54 - H5-Current-Limit	6000	16-Bit		
DPL-F1 - OUT1_STAT	0	2-Bit		CTRL1_0x54 - H6-Current-Limit	6000	16-Bit		
DPL-F1 - OUT2_STAT	0	2-Bit		CTRL1_0x55 - COMMAND_85	85	8-Bit		
DPL-F1 - OUT3_STAT	0	2-Bit		CTRL1_0x55 - H7-Current-Limit	6000	16-Bit		
DPL-F1 - OUT4_STAT	0	2-Bit		CTRL1_0x55 - H8-Current-Limit	6000	16-Bit		
DPL-F1 - OUT5_STAT	0	2-Bit		CTRL1_0x56 - COMMAND_86	86	8-Bit		
DPL-F1 - OUT6_STAT	0	2-Bit		CTRL1_0x56 - H1-Inrush-Allowance	0	4-Bit		
DPL-F1 - OUT7_STAT	0	2-Bit		CTRL1_0x56 - H2-Inrush-Allowance	0	4-Bit		
DPL-F1 - OUT8_STAT	0	2-Bit		CTRL1_0x56 - H3-Inrush-Allowance	0	4-Bit		
DPL-F1 - NODE_FAULT	0	2-Bit		CTRL1_0x56 - H4-Inrush-Allowance	0	4-Bit		
DPL-F1 - OUT1_LIMIT	0	2-Bit		CTRL1_0x56 - H5-Inrush-Allowance	0	4-Bit		
DPL-F1 - OUT2_LIMIT	0	2-Bit		CTRL1_0x56 - H6-Inrush-Allowance	0	4-Bit		
DPL-F1 - OUT3_LIMIT	0	2-Bit		CTRL1_0x56 - H7-Inrush-Allowance	0	4-Bit		
DPL-F1 - OUT4_LIMIT	0	2-Bit		CTRL1_0x56 - H8-Inrush-Allowance	0	4-Bit		
DPL-F1 - OUT5_LIMIT	0	2-Bit		CTRL1_0x57 - COMMAND_87	87	8-Bit		
DPL-F1 - OUT6 LIMIT	0	2-Bit		CTRL1_0x57 - H1-Limit-Reaction	1	4-Bit		

**Step 4:** Click on Setting and select the CAN device you are connected you are using to connect to the module. If you are using a Data Panel UCG, select **DP-UCG**. If you are using a Peak Tool, select **PCAN-USB**.

You can also select the **BAUD rate** you are using and the **Source Address Offset** for your connected device.

Click "OK" when finished.

In this example, we are using a Peak tool to connect. Our module is set at a BAUD rate of 250k and at a Source Address Offset of 0.

3it 🛛		CTRL1_0x52 - ID1
Bit		CTRL1_0x53 - COMMAND_8
Bit	Settings	× urrent-Li
Bit		urrent-Li
Bit	CAN Device	Bit-Rate (Bit/s)
Bit	C DP-UCG	250000 T MAND 8
Bit	PCAN-USB	urrent-Li
Bit		urrent-Li
t	UCG COM Port	Source Address Offset
t	AUTO	MAND 8
t		urrent-Li
t		urrent-Li
t	Startup Warning	MAND 8
+	Advanced Mode	OK Cancel About Allo
L 4	Expermental Features	
1		DOTRET_0x50 - H2-Infush-Allo
T		III IRI I IIV56 - H3-Inriich-ΔΙΙ

**Step 5:** Once the module is hooked up, click 'Connect' on DPNPlayer tool bar, and the left column should turn green, as seen in the image below.

Receiing Inputs					Transmitting Outputs.				
Input Name	Value	Туре	Info	^	Output Name	Value	Type	Info	
AD2 - CNEG1	327	10-Bit-2			CTBL1_0x57 - H8-Limit-Reaction	1	4-Bit		
AD2 - VBAT	657	10-Bit-2			CTRL1 0xF2 - COMMAND 226	226	8-Bit		
AD2 - TEMP	91	10-Bit-2			CTRL1 0xE2 - UNLOCK KEY1	81	8-Bit		
AD3 - FET1x	0	16-Bit			CTRL1 0xE2 - UNLOCK KEY2	80	8-Bit		
AD3 - FET2x	0	16-Bit			CTRL1 0xE2 - SOURCE ADDRESS	0	8-Bit		
AD3 - FET3x	0	16-Bit			CTRL1 0xE2 - Reboot	0	2-Bit		
AD3 - FET4x	0	16-Bit			CTRL1 0xE4 - COMMAND 228	228	8-Bit		
AD4 - FET5x	0	16-Bit			CTRL1 0xE4 - UNLOCK KEY1	81	8-Bit		
AD4 - FET6x	0	16-Bit			CTRL1 0xE4 - UNLOCK KEY2	80	8-Bit		
AD4 - FET7x	0	16-Bit			CTRL1_0xE4 - OUTPUT_INDEX	0	8-Bit		
AD4 - FET8x	0	16-Bit			CTRL1_0xE4 - DURATION	10	8-Bit		
DPL-F1 - OUT1_STAT	0	2-Bit			CTRL1_0xE4 - LOAD_VALUE	5000	16-Bit		
DPL-F1 - OUT2_STAT	0	2-Bit			CTRL1_0xE4 - Set Upper	0	2-Bit		
DPL-F1 - OUT3_STAT	0	2-Bit			CTRL1_0xE4 - OEM Reset	0	2-Bit		
DPL-F1 - OUT4_STAT	0	2-Bit			CTRL1 - COMMAND_81	81	8-Bit		
DPL-F1 - OUT5_STAT	0	2-Bit			CTRL1 - Output 1A	0	2-Bit		
DPL-F1 - OUT6_STAT	0	2-Bit			CTRL1 - Output 1B	0	2-Bit		
DPL-F1 - OUT7_STAT	0	2-Bit			CTRL1 - Output 2A	0	2-Bit		
DPL-F1 - OUT8_STAT	0	2-Bit			CTRL1 - Output 2B	0	2-Bit		
DPL-F1 - NODE_FAULT	0	2-Bit			CTRL1 - Output 3A	0	2-Bit		
DPL-F1 - OUT1_LIMIT	0	2-Bit			CTRL1 - Output 3B	0	2-Bit		
DPL-F1 - OUT2_LIMIT	0	2-Bit			CTRL1 - Output 4A	0	2-Bit		
DPL-F1 - OUT3_LIMIT	0	2-Bit			CTRL1 - Output 4B	0	2-Bit		
DPL-F1 - OUT4_LIMIT	0	2-Bit			CTRL1 - Output 5A	0	2-Bit		
DPL-F1 - OUT5_LIMIT	0	2-Bit			CTRL1 - Output 5B	0	2-Bit		
DPL-F1 - OUT6_LIMIT	0	2-Bit			CTRL1 - Output 6A	0	2-Bit		
DPL-F1 - OUT7_LIMIT	0	2-Bit			CTRL1 - Output 6B	0	2-Bit		
DPL-F1 - OUT8_LIMIT	0	2-Bit		~	CTRL1 - Output 7A	0	2-Bit		

NOTE: ANY OUTPUT VALUES IN GRAY TAKE EFFECT ONLY WHEN 'SEND' IS CLICKED.

**Step 6:** To have configuration settings saved after a power cycle, ensure 'Save Configuration' is checked.

Receving Inputs					Transmitting Outputs. I Status						
Input Name	Value	Туре	Info		Output Name	Value	Туре	Info			
AD2 - CNFG1	327	10-Bit-2			CTRL1 0x52 - COMMAND 82	82	8-Bit				
AD2 - VBAT	488	10-Bit-2		<b>F</b>	CTRL1 0x52 - Save Configuration	1	2-Bit				
AD2 - TEMP	99	10-Bit-2			CTRL1 0x52 - Unprocessed Analog Inputs	0	2-Bit				
AD3 - FET1x	0	16-Bit			CTRL1_0x52 - ID1	0	8-Bit				
AD3 - FET2x	0	16-Bit			CTRL1_0x53 - COMMAND_83	83	8-Bit				
AD3 - FET3x	0	16-Bit			CTRL1_0x53 - H1-Current-Limit	6000	16-Bit				
AD3 - FET4x	0	16-Bit			CTRL1_0x53 - H2-Current-Limit	6000	16-Bit				
AD4 - FET5x	0	16-Bit			CTRL1_0x53 - H3-Current-Limit	6000	16-Bit				
AD4 - FET6x	0	16-Bit			CTRL1_0x54 - COMMAND_84	84	8-Bit				
AD4 - FET7x	0	16-Bit			CTRL1_0x54 - H4-Current-Limit	6000	16-Bit				
AD4 - FET8x	0	16-Bit			CTRL1_0x54 - H5-Current-Limit	6000	16-Bit				
DPL-F1 - OUT1_STAT	0	2-Bit			CTRL1_0x54 - H6-Current-Limit	6000	16-Bit				
DPL-F1 - OUT2_STAT	0	2-Bit			CTRL1_0x55 - COMMAND_85	85	8-Bit				
DPL-F1 - OUT3_STAT	0	2-Bit			CTRL1_0x55 - H7-Current-Limit	6000	16-Bit				
DPL-F1 - OUT4_STAT	0	2-Bit			CTRL1_0x55 - H8-Current-Limit	6000	16-Bit				
DPL-F1 - OUT5_STAT	0	2-Bit			CTRL1_0x56 - COMMAND_86	86	8-Bit				
DPL-F1 - OUT6_STAT	0	2-Bit			CTRL1_0x56 - H1-Inrush-Allowance	0	4-Bit				
DPL-F1 - OUT7_STAT	0	2-Bit			CTRL1_0x56 - H2-Inrush-Allowance	0	4-Bit				
DPL-F1 - OUT8_STAT	0	2-Bit			CTRL1_0x56 - H3-Inrush-Allowance	0	4-Bit				
DPL-F1 - NODE_FAULT	0	2-Bit			CTRL1_0x56 - H4-Inrush-Allowance	0	4-Bit				
DPL-F1 - OUT1_LIMIT	0	2-Bit			CTRL1_0x56 - H5-Inrush-Allowance	0	4-Bit				
DPL-F1 - OUT2_LIMIT	0	2-Bit			CTRL1_0x56 - H6-Inrush-Allowance	0	4-Bit				
DPL-F1 - OUT3_LIMIT	0	2-Bit			CTRL1_0x56 - H7-Inrush-Allowance	0	4-Bit				
DPL-F1 - OUT4_LIMIT	0	2-Bit			CTRL1_0x56 - H8-Inrush-Allowance	0	4-Bit				
DPL-F1 - OUT5_LIMIT	0	2-Bit			CTRL1_0x57 - COMMAND_87	87	8-Bit				
DPL-F1 - OUT6_LIMIT	0	2-Bit			CTRL1_0x57 - H1-Limit-Reaction	1	4-Bit				
DPL-F1 - OUT7_LIMIT	0	2-Bit			CTRL1_0x57 - H2-Limit-Reaction	1	4-Bit				
DPL-F1 - OUT8_LIMIT	0	2-Bit			CTRL1_0x57 - H3-Limit-Reaction	1	4-Bit				
STAT - Response	0	8-Bit			CTRL1_0x57 - H4-Limit-Reaction	1	4-Bit				
STAT - R1	0	8-Rit		~	CTRL 1 0x57 - H5-Limit-Reaction	1	<b>⊿</b> _Rit				

**Step 7:** To monitor individual output currents and have a configurable response when a current limit has been exceeded, you can set the User Current Limit using '**Hx-Current-Limit'**. (Note: 1000 = 1 Amp)

In this example, the configurable response will be triggered when HB1 exceeds 25 Amps.

DPNPlayer v2.2								-		×
🚰 File 🗸 🜷 Disconnect  ⊳ Sens	d 🔠 Plu	gins 👻 🎯 Se	ttings							
Receiing Inputs				Transmit	ting Outputs.					
				25000	-					
										_
					/					-
Input Name	Value	Туре	Info		it Name		Value	Туре	Info	
AD2 - CNFG1	327	10-Bit-2		CTR	L1 0x52 - COMMAND 82		82	8-Bit		
AD2 - VBAT	494	10-Bit-2		CTR	L1 0x52 - Save Configurat	tion	0	2-Bit		
AD2 - TEMP	115	10-Bit-2		CTR	L1_0x52 - Unprocessed Ar	nalog Inputs	0	2-Bit		
AD3 - FET1x	0	16-Bit			L1_0x52 - ID1		0	8-Bit		
AD3 - FET2x	0	16-Bit		✓ CTR	L1_0x53 - COMMAND_83		83	8-Bit		
AD3 - FET3x	0	16-Bit			L1_0x53 - H1-Current-Lim	L	20000	то-віт		
AD3 - FET4x	0	16-Bit		■ CTR	L1_0x53 H2 Current Limi	t	6000	16 Bit		-
AD4 - FET5x	0	16-Bit		✓ CTR	L1_0x53 - H3-Current-Limi	t	6000	16-Bit		
AD4 - FET6x	0	16-Bit		✓ CTR	L1_0x54 - COMMAND_84		84	8-Bit		
AD4 - FET7x	0	16-Bit		✓ CTR	L1_0x54 - H4-Current-Limi	t	6000	16-Bit		
AD4 - FET8x	0	16-Bit		✓ CTR	L1_0x54 - H5-Current-Limi	t	6000	16-Bit		
DPL-F1 - OUT1_STAT	0	2-Bit		Image: CTR	L1_0x54 - H6-Current-Limi	t	6000	16-Bit		
DPL-F1 - OUT2_STAT	0	2-Bit		Image: CTR	L1_0x55 - COMMAND_85		85	8-Bit		
DPL-F1 - OUT3_STAT	0	2-Bit		Image: CTR	L1_0x55 - H7-Current-Limi	t	6000	16-Bit		
DPL-F1 - OUT4_STAT	0	2-Bit		☑ CTR	L1_0x55 - H8-Current-Limi	t	6000	16-Bit		
DPL-F1 - OUT5_STAT	0	2-Bit		Image: CTR	L1_0x56 - COMMAND_86		86	8-Bit		
DPL-F1 - OUT6_STAT	0	2-Bit		CTR	L1_0x56 - H1-Inrush-Allow	ance	0	4-Bit		
DPL-F1 - OUT7_STAT	0	2-Bit			L1_0x56 - H2-Inrush-Allow	ance	0	4-Bit		
DPL-F1 - OUT8_STAT	0	2-Bit		CTR	L1_0x56 - H3-Inrush-Allow	ance	0	4-Bit		
DPL-F1 - NODE_FAULT	0	2-Bit		CTR	L1_0x56 - H4-Inrush-Allow	ance	0	4-Bit		
DPL-F1 - OUT1_LIMIT	0	2-Bit			L1_0x56 - H5-Inrush-Allow	ance	0	4-Bit		
DPL-F1 - OUT2_LIMIT	0	2-Bit			L1_0x56 - H6-Inrush-Allow	ance	0	4-Bit		
UPL-F1 - OUT3_LIMIT	0	2-Bit		LICTR	L1_0x56 - H7-Inrush-Allow	ance	0	4-Bit		
DPL-F1-OUT4_LIMIT	0	2-Bit		LCTR	L1_0x56 - H8-Inrush-Allow	ance	0	4-Bit		
DPL-F1 - OUT5_LIMIT	0	2-Bit		✓ CTR	L1_0x57 - COMMAND_87		87	8-Bit		
JDPL-F1 - OUT6 LIMIT	U	2-Bit		~ <					,	>



#### NOTE: IF NOTHING IS SET, MODULE WILL USE HARDWARE LIMIT (29 AMPS).

**Step 8:** To set a time limit for an inrush that exceeds the set current limit, you can adjust the Inrush Allowance using **'Hx-Inrush-Allowance'**. (Note: Value is in <u>100ms Increments</u>)

In this example, we set it for 1.5 seconds before the limit reaction is triggered.

Garne - Bisconnect D Send	. 888 . 10	Aura - Sea ac	ungs					
Receiving Inputs					15			
Input Name	Value	Туре	Info	^	Output Name	Value	Туре	Info
AD2 - CNFG1	327	10-Bit-2			CTRL1 0x54 - H4-Current-Limit	6000	16-Bit	
AD2 - VBAT	494	10-Bit-2			CTRL1 0x54 - H5-Current-Limit	6000	16-Bit	
AD2 - TEMP	116	10-Bit-2			CTRL1 0x54 - H6-Current-Limit	6000	16-Bit	
AD3 - FET1x	0	16-Bit			CTRL1 0x55 - COMMAND 85	85	8-Bit	
AD3 - FET2x	0	16-Bit			CTRL1 0x55 - H7-Current-Limit	6000	16-Bit	
AD3 - FET3x	0	16-Bit			CTRL1 0x55 - H8-Current-Limit	6000	16-Bit	
AD3 - FET4x	0	16-Bit			CTRL1 0x56 - COMMAND 86	86	8-Bit	
AD4 - FET5x	0	16-Bit			CTRL1 0x56 - H1-Inrush-Allowance	15	4-Bit	
AD4 - FET6x	0	16-Bit			CTRL1 0x56 - H2-Inrush-Allowance	0	4-Bit	
AD4 - FET7x	0	16-Bit			CTRL1_0x56 - H3-Inrush-Allowance	0	4-Bit	
AD4 - FET8x	0	16-Bit			CTRL1_0x56 - H4-Inrush-Allowance	0	4-Bit	
DPL-F1 - OUT1 STAT	0	2-Bit			CTRL1 0x56 - H5-Inrush-Allowance	0	4-Bit	
DPL-F1 - OUT2 STAT	0	2-Bit			CTRL1 0x56 - H6-Inrush-Allowance	0	4-Bit	
DPL-F1 - OUT3_STAT	0	2-Bit			CTRL1_0x56 - H7-Inrush-Allowance	0	4-Bit	
DPL-F1 - OUT4_STAT	0	2-Bit			CTRL1_0x56 - H8-Inrush-Allowance	0	4-Bit	
DPL-F1 - OUT5_STAT	0	2-Bit			CTRL1_0x57 - COMMAND_87	87	8-Bit	
DPL-F1 - OUT6_STAT	0	2-Bit			CTRL1_0x57 - H1-Limit-Reaction	1	4-Bit	
DPL-F1 - OUT7_STAT	0	2-Bit			CTRL1_0x57 - H2-Limit-Reaction	1	4-Bit	
DPL-F1 - OUT8_STAT	0	2-Bit			CTRL1_0x57 - H3-Limit-Reaction	1	4-Bit	
DPL-F1 - NODE_FAULT	0	2-Bit			CTRL1_0x57 - H4-Limit-Reaction	1	4-Bit	
DPL-F1 - OUT1_LIMIT	0	2-Bit			CTRL1_0x57 - H5-Limit-Reaction	1	4-Bit	
DPL-F1 - OUT2_LIMIT	0	2-Bit			CTRL1_0x57 - H6-Limit-Reaction	1	4-Bit	
DPL-F1 - OUT3_LIMIT	0	2-Bit			CTRL1_0x57 - H7-Limit-Reaction	1	4-Bit	
DPL-F1 - OUT4_LIMIT	0	2-Bit			CTRL1_0x57 - H8-Limit-Reaction	1	4-Bit	
DPL-F1 - OUT5_LIMIT	0	2-Bit			CTRL1_0xE2 - COMMAND_226	226	8-Bit	
DPL-F1 - OUT6 LIMIT	0	2-Bit		~	<			>



**Step 9:** To configure the module's response when exceeding the set current limit and inrush time allowance, adjust the value for '**HX-Limit-Reaction'**. (0-2)

👘 File 🗸 👗 Disconnect 🕞 Send	I III Pluc	jins 👻 🙆 Se	ttings							
Receiing Inputs		-9-			□ [Transmitting Outputs.] □					
Input Name	Value	Туре	Info	^	Output Name	Value	Туре	Info	^	
AD2 - CNFG1	327	10-Bit-2			CTRL1 0x56 - COMMAND 86	86	8-Bit		_	
☑ AD2 - VBAT	494	10-Bit-2			CTRL1 0x56 - H1-Inrush-Allowance	15	4-Bit			
☑ AD2 - TEMP	116	10-Bit-2			CTRL1 0x56 - H2-Inrush-Allowance	0	4-Bit			
AD3 - FET1x	0	16-Bit			CTRL1_0x56 - H3-Inrush-Allowance	0	4-Bit			
AD3 - FET2x	0	16-Bit			CTRL1_0x56 - H4-Inrush-Allowance	0	4-Bit			
AD3 - FET3x	0	16-Bit			CTRL1_0x56 - H5-Inrush-Allowance	0	4-Bit			
AD3 - FET4x	0	16-Bit			CTRL1 0x56 - H6-Inrush-Allowance	0	4-Bit			
AD4 - FET5x	0	16-Bit			CTRL1_0x56 - H7-Inrush-Allowance	0	4-Bit			
AD4 - FET6x	0	16-Bit			CTRL1_0x56 - H8-Inrush-Allowance	0	4-Bit			
AD4 - FET7x	0	16-Bit			CTRL1 0x57 - COMMAND 87	87	8-Bit			
AD4 - FET8x	0	16-Bit			CTRL1 0x57 - H1-Limit-Reaction	1	4-Bit			
DPL-F1 - OUT1_STAT	0	2-Bit			CTRL1_0x57 - H2-Limit-Reaction	1	4-Bit			
DPL-F1 - OUT2_STAT	0	2-Bit			CTRL1_0x57 - H3-Limit-Reaction	1	4-Bit			
DPL-F1 - OUT3_STAT	0	2-Bit			CTRL1_0x57 - H4-Limit-Reaction	1	4-Bit			
DPL-F1 - OUT4 STAT	0	2-Bit			CTRL1 0x57 - H5-Limit-Reaction	1	4-Bit			
DPL-F1 - OUT5_STAT	0	2-Bit			CTRL1_0x57 - H6-Limit-Reaction	1	4-Bit			
DPL-F1 - OUT6_STAT	0	2-Bit			CTRL1_0x57 - H7-Limit-Reaction	1	4-Bit			
DPL-F1 - OUT7_STAT	0	2-Bit			CTRL1_0x57 - H8-Limit-Reaction	1	4-Bit			
DPL-F1 - OUT8_STAT	0	2-Bit			CTRL1_0xE2 - COMMAND_226	226	8-Bit			
DPL-F1 - NODE_FAULT	0	2-Bit			CTRL1_0xE2 - UNLOCK_KEY1	81	8-Bit			
DPL-F1 - OUT1_LIMIT	0	2-Bit			CTRL1_0xE2 - UNLOCK_KEY2	80	8-Bit			
DPL-F1 - OUT2_LIMIT	0	2-Bit			CTRL1_0xE2 - SOURCE_ADDRESS	0	8-Bit			
DPL-F1 - OUT3_LIMIT	0	2-Bit			CTRL1_0xE2 - Reboot	0	2-Bit			
DPL-F1 - OUT4_LIMIT	0	2-Bit			CTRL1_0xE4 - COMMAND_228	228	8-Bit			
DPL-F1 - OUT5_LIMIT	0	2-Bit			CTRL1_0xE4 - UNLOCK_KEY1	81	8-Bit			
DPL-F1 - OUT6 LIMIT	0	2-Bit		~	<				>	

In this example, the Limit reaction is set to 'Output Stall Current'. (Refer to next page.)

ID	Current Limit Response Description
0	Disable User Current Limit
1	Output Stall
2	Output Over Current

When configured as **'Output Stall'** ("1"):

- When the User Current Limit is exceeded, the output shall shut off within 3ms.
- The output indicator will flash, and the event will be indicated on the CANbus.
- The Output Stall will be cleared when the output is commanded off.
- Output Stall shall be the default behavior.

When configured as 'Output Overcurrent' ("2"):

- When the User Current Limit is exceeded, the output shall shut off within 3ms.
- The output indicator will flash, and an output overcurrent code will be set, and the event will be indicated on the CANbus.
- A power cycle shall be required to reset over current faults and restore operation.

**Step 10:** To change the module's source address, adjust the **'Source\_Address'** value.

🔊 DPNPlayer v2.2							-		×		
📬 File 🗸 븷 Disconnect ⊳ Seno	Plug	gins 👻 🛞 Se	ttings								
Receiing Inputs					Transmitting Outputs.						
					237 🔅						
									_		
Input Name	Value	Туре	Info	^	Output Name	Value	Туре	Info			
AD2 - CNFG1	327	10-Bit-2			CTRL1 0x56 - H6-Inrush-Allowance	0	4-Bit				
AD2 - VBAT	494	10-Bit-2			CTRL1_0x56 - H7-Inrush-Allowance	0	4-Bit				
AD2 - TEMP	117	10-Bit-2			CTRL1 0x56 - H8-Inrush-Allowance	0	4-Bit				
AD3 - FET1x	0	16-Bit			CTRL1_0x57 - COMMAND_87	87	8-Bit				
AD3 - FET2x	0	16-Bit			CTRL1_0x57 - H1-Limit-Reaction	1	4-Bit				
AD3 - FET3x	0	16-Bit			CTRL1_0x57 - H2-Limit-Reaction	1	4-Bit				
AD3 - FET4x	0	16-Bit			CTRL1_0x57 - H3-Limit-Reaction	1	4-Bit				
AD4 - FET5x	0	16-Bit			CTRL1_0x57 - H4-Limit-Reaction	1	4-Bit				
AD4 - FET6x	0	16-Bit			CTRL1_0x57 - H5-Limit-Reaction	1	4-Bit				
AD4 - FET7x	0	16-Bit			CTRL1_0x57 - H6-Limit-Reaction	1	4-Bit				
AD4 - FET8x	0	16-Bit			CTRL1_0x57 - H7-Limit-Reaction	1	4-Bit				
DPL-F1 - OUT1_STAT	0	2-Bit			CTRL1_0x57 - H8-Limit-Reaction	1	4-Bit				
DPL-F1 - OUT2_STAT	0	2-Bit			CTRL1_0xE2 - COMMAND_226	226	8-Bit				
DPL-F1 - OUT3_STAT	0	2-Bit			CTRL1_0xE2 - UNLOCK_KEY1	81	8-Bit				
DPL-F1 - OUT4_STAT	0	2-Bit			CTRL1 0xE2 - UNLOCK KEY2	80	8-Bit				
DPL-F1 - OUT5_STAT	0	2-Bit			CTRL1_0xE2 - SOURCE_ADDRESS	237	8-Bit				
DPL-F1 - OUT6_STAT	0	2-Bit			CTRL1_0xE2 - Reboot	0	2-Bit				
DPL-F1 - OUT7_STAT	0	2-Bit			CTRL1_0xE4 - COMMAND_228	228	8-Bit				
DPL-F1 - OUT8_STAT	0	2-Bit			CTRL1_0xE4 - UNLOCK_KEY1	81	8-Bit				
DPL-F1 - NODE_FAULT	0	2-Bit			CTRL1_0xE4 - UNLOCK_KEY2	80	8-Bit				
DPL-F1 - OUT1_LIMIT	0	2-Bit			CTRL1_0xE4 - OUTPUT_INDEX	0	8-Bit				
DPL-F1 - OUT2_LIMIT	0	2-Bit			CTRL1_0xE4 - DURATION	10	8-Bit				
DPL-F1 - OUT3_LIMIT	0	2-Bit			CTRL1_0xE4 - LOAD_VALUE	5000	16-Bit				
DPL-F1 - OUT4_LIMIT	0	2-Bit			CTRL1_0xE4 - Set Upper	0	2-Bit				
DPL-F1 - OUT5_LIMIT	0	2-Bit			CTRL1_0xE4 - OEM Reset	0	2-Bit				
DPL-F1 - OUT6 LIMIT	0	2-Bit		~	<				>		

In this example, the Source Address is set as 237 (0xED).



# NOTE: THE RANGE FOR SOURCE ADDRESSES ARE **128-247 (0x80-0xF7).** DEFAULT IS **237 (0xED).** KEEP UNCHECKED TO LEAVE SOURCE ADDRESS AS DEFAULT.

#### **Step 11:** For all above changes to take into effect, click 'Send'.

Receiing Inputs				Transmitting Outputs.				
Include Name	Malua	Trees	1-1-	Output Nama	Velue	Trees	1-6-	
	value	Type	inio	 Output Name	value	1ype	Inio	_
AD2 VRAT	327	10-DII-2		CTRL1_0x57 - Ho-LIMIL-Reaction	1	4-DIL 9 Dit		
AD2 TEMD	03/	10-BIL-2		CIRCIDUEZ - COMMAND_220	220	o-Dil		
	91	10-DIL-2		CTRLI_UXE2 - UNLOCK_KEY2	01	0-Dit		
	0	16 Bit			0	9 Bit		
	0	16-Bit		CTRL1_0xE2 - SOURCE_ADDRESS	0	2-Bit		
AD3 - FET4x	0	16-Bit		CTRL1_0xE4 - COMMAND 228	228	8-Bit		
AD4 - FET5x	0	16-Bit		CTRL1 0xE4 - UNLOCK KEY1	81	8-Bit		
AD4 - FFT6x	0	16-Bit		CTRL1 0xE4 - UNLOCK KEY2	80	8-Bit		
AD4 - FFT7x	0	16-Bit		CTRI 1 0xE4 - OUTPUT INDEX	0	8-Bit		
AD4 - FET8x	0	16-Bit		CTRL1 0xE4 - DURATION	10	8-Bit		
DPL-F1 - OUT1 STAT	0	2-Bit		CTRL1 0xE4 - LOAD VALUE	5000	16-Bit		
DPL-F1 - OUT2 STAT	0	2-Bit		CTRL1 0xE4 - Set Upper	0	2-Bit		
DPL-F1 - OUT3 STAT	0	2-Bit		CTRL1 0xE4 - OEM Reset	0	2-Bit		
DPL-F1 - OUT4 STAT	0	2-Bit		CTRL1 - COMMAND 81	81	8-Bit		
DPL-F1 - OUT5 STAT	0	2-Bit		CTRL1 - Output 1A	0	2-Bit		
DPL-F1 - OUT6_STAT	0	2-Bit		CTRL1 - Output 1B	0	2-Bit		
DPL-F1 - OUT7 STAT	0	2-Bit		CTRL1 - Output 2A	0	2-Bit		
DPL-F1 - OUT8_STAT	0	2-Bit		CTRL1 - Output 2B	0	2-Bit		
DPL-F1 - NODE_FAULT	0	2-Bit		CTRL1 - Output 3A	0	2-Bit		
DPL-F1 - OUT1_LIMIT	0	2-Bit		CTRL1 - Output 3B	0	2-Bit		
DPL-F1 - OUT2_LIMIT	0	2-Bit		CTRL1 - Output 4A	0	2-Bit		
DPL-F1 - OUT3_LIMIT	0	2-Bit		CTRL1 - Output 4B	0	2-Bit		
DPL-F1 - OUT4_LIMIT	0	2-Bit		CTRL1 - Output 5A	0	2-Bit		
DPL-F1 - OUT5_LIMIT	0	2-Bit		CTRL1 - Output 5B	0	2-Bit		
DPL-F1 - OUT6_LIMIT	0	2-Bit		CTRL1 - Output 6A	0	2-Bit		
DPL-F1 - OUT7_LIMIT	0	2-Bit		CTRL1 - Output 6B	0	2-Bit		
TODI E1 OUT9 LIMIT	0	2_Bit		 CTRL1 - Output 7A	0	2-Bit		

**Step 11:** To turn on individual H-Bridge outputs, check the box for '**CTRL1 – Output XY'**. (Note: A is channel is forward, B channel is reverse.)

In this example, the <u>forward</u> direction output for H-Bridge 1 is turned on. (A Channel)

- Inc - Pisconnect P send	100 PIU	Aura . 200, 26	unga								
Receiing Inputs					Transmitting Outputs. ▼ Status						
Input Name	Value	Туре	Info	^	Output Name	Value	Туре	Info			
AD2 - CNFG1	327	10-Bit-2			CTRL1 0xE4 - UNLOCK KEY1	81	8-Bit				
AD2 - VBAT	494	10-Bit-2			CTRL1 0xE4 - UNLOCK KEY2	80	8-Bit				
AD2 - TEMP	117	10-Bit-2			CTRL1 0xE4 - OUTPUT INDEX	0	8-Bit				
AD3 - FET1x	0	16-Bit			CTRL1_0xE4 - DURATION	10	8-Bit				
AD3 - FET2x	0	16-Bit			CTRL1 0xE4 - LOAD VALUE	5000	16-Bit				
AD3 - FET3x	0	16-Bit			CTRL1_0xE4 - Set Upper	0	2-Bit				
AD3 - FET4x	0	16-Bit			CTRL1_0xE4 - OEM Reset	0	2-Bit				
AD4 - FET5x	0	16-Bit			CTRL1 - COMMAND 81	81	8-Bit	_			
AD4 - FET6x	0	16-Bit			CTRL1 - Output 1A	1	2-Bit				
AD4 - FET7x	0	16-Bit			CTRL1 - Output 1B	0	2-Bit				
AD4 - FET8x	0	16-Bit			CTRL1 - Output 2A	0	2-Bit				
DPL-F1 - OUT1_STAT	1	2-Bit			CTRL1 - Output 2B	0	2-Bit				
DPL-F1 - OUT2_STAT	0	2-Bit			CTRL1 - Output 3A	0	2-Bit				
DPL-F1 - OUT3_STAT	0	2-Bit			CTRL1 - Output 3B	0	2-Bit				
DPL-F1 - OUT4_STAT	0	2-Bit			CTRL1 - Output 4A	0	2-Bit				
DPL-F1 - OUT5_STAT	0	2-Bit			CTRL1 - Output 4B	0	2-Bit				
DPL-F1 - OUT6_STAT	0	2-Bit			CTRL1 - Output 5A	0	2-Bit				
DPL-F1 - OUT7_STAT	0	2-Bit			CTRL1 - Output 5B	0	2-Bit				
DPL-F1 - OUT8_STAT	0	2-Bit			CTRL1 - Output 6A	0	2-Bit				
DPL-F1 - NODE_FAULT	0	2-Bit			CTRL1 - Output 6B	0	2-Bit				
DPL-F1 - OUT1_LIMIT	0	2-Bit			CTRL1 - Output 7A	0	2-Bit				
DPL-F1 - OUT2_LIMIT	0	2-Bit			CTRL1 - Output 7B	0	2-Bit				
DPL-F1 - OUT3_LIMIT	0	2-Bit			CTRL1 - Output 8A	0	2-Bit				
DPL-F1 - OUT4_LIMIT	0	2-Bit			CTRL1 - Output 8B	0	2-Bit				
DPL-F1 - OUT5_LIMIT	0	2-Bit			CTRL1 - Pair_H1&H2	0	2-Bit				
DPL-F1 - OUT6 LIMIT	0	2-Bit		~	<			>			

In this example, the <u>reverse</u> direction output for H-Bridge 1 is turned on. (B Channel)

-Receiving Inputs					□ Transmitting Outputs. I I Status						
Input Name	Value	Turno	Info		Output Nama	Value	Turne	Info			
	value	10 Bit 2	IIIIO			Value 91	o Dit	IIIO			
AD2 VPAT	327	10-Dit-2				01	0-DIL 0 Dit				
	494	10-Dit-2				0	8 Bit				
	0	16-Bit				10	8_Bit				
	0	16-Bit			CTRL1 0yE4 - LOAD VALUE	5000	16_Bit				
AD3 - FFT3x	0	16-Bit			CTRL1 0xE4 - Set Upper	0	2-Bit				
AD3 - FET4x	0	16-Bit			CTRI 1 0xE4 - OEM Reset	0	2-Bit				
AD4 - FET5x	0	16-Bit			CTRL1-COMMAND 81	81	8-Bit				
AD4 - FET6x	0	16-Bit			CTRL1 - Output 1A	0	2-Bit				
AD4 - FET7x	0	16-Bit			CTRL1 - Output 1B	1	2-Bit				
AD4 - FET8x	0	16-Bit			CTRL1 - Output 2A	0	2-Bit				
DPL-F1 - OUT1 STAT	1	2-Bit			CTRL1 - Output 2B	0	2-Bit				
DPL-F1 - OUT2 STAT	0	2-Bit			CTRL1 - Output 3A	0	2-Bit				
DPL-F1 - OUT3_STAT	0	2-Bit			CTRL1 - Output 3B	0	2-Bit				
DPL-F1 - OUT4_STAT	0	2-Bit			CTRL1 - Output 4A	0	2-Bit				
DPL-F1 - OUT5_STAT	0	2-Bit			CTRL1 - Output 4B	0	2-Bit				
DPL-F1 - OUT6_STAT	0	2-Bit			CTRL1 - Output 5A	0	2-Bit				
DPL-F1 - OUT7_STAT	0	2-Bit			CTRL1 - Output 5B	0	2-Bit				
DPL-F1 - OUT8_STAT	0	2-Bit			CTRL1 - Output 6A	0	2-Bit				
DPL-F1 - NODE_FAULT	0	2-Bit			CTRL1 - Output 6B	0	2-Bit				
DPL-F1 - OUT1_LIMIT	0	2-Bit			CTRL1 - Output 7A	0	2-Bit				
DPL-F1 - OUT2_LIMIT	0	2-Bit			CTRL1 - Output 7B	0	2-Bit				
DPL-F1 - OUT3_LIMIT	0	2-Bit			CTRL1 - Output 8A	0	2-Bit				
DPL-F1 - OUT4_LIMIT	0	2-Bit			CTRL1 - Output 8B	0	2-Bit				
DPL-F1 - OUT5_LIMIT	0	2-Bit			CTRL1 - Pair_H1&H2	0	2-Bit				
DPL-F1 - OUT6 LIMIT	0	2-Bit		~	<			>			

For more information, scan the below QR codes for Datasheets:





